

Dkt. 2271/70977

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Hitoshi YAMAMOTO

Serial No.: 10/656,434

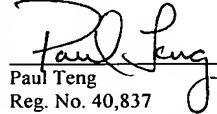
Group Art Unit: 2826

Date Filed: September 5, 2003

Examiner: Alexander O. Williams

For: SEMICONDUCTOR DEVICE RESPONSIVE TO DIFFERENT LEVELS OF INPUT
AND OUTPUT SIGNALS AND SIGNAL PROCESSING SYSTEM USING THE
SAME

I hereby certify that this correspondence is being deposited this date with
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Paul Teng

Reg. No. 40,837

November 18, 2005

Date

1185 Avenue of the Americas
New York, N.Y. 10036
(212) 278-0400

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

INFORMATION DISCLOSURE STATEMENT

Pursuant to the applicant's duty of disclosure, the information listed in the attached Form PTO-1449 is brought to the attention of the Examiner.

It is respectfully requested that the information cited in annexed Form PTO-1449 be considered by the Examiner in connection with the above-identified patent application, and that such art be made of record in said application.

The citation of the listed items is not a representation that they constitute a complete or exhaustive listing of the relevant art or that these items are prior art. The items listed are submitted in good faith, but are not intended to substitute for the Examiner's search. It is hoped, however, that in addition to apprising the Examiner of the particular items, they will assist in identifying fields of search and in making as full and complete a search as possible.

The filing of this Information Disclosure Statement is not an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. §1.56(b).

According to the August 5, 2003 OG Notice published by the Patent and Trademark Office, the Office has waived the requirement under 37 C.F.R. §1.98(a)(2)(i) for submitting a copy of each cited U.S. patent and each cited U.S. patent application publication for all U.S. national patent applications filed after June 30, 2003 and for all international application that have entered the national stage under 35 U.S.C. §371 after June 30, 2003.

Accordingly, copies are submitted herewith for only references listed on the annexed Form PTO-1449 that are not a U.S. patent or U.S. patent application publication.

A Notice Of Allowance dated September 2, 2005 was issued by the U.S. Patent Office in connection with this application. Applicant has not yet paid the issue fee which is due December 2, 2005.

This Information Disclosure Statement is being filed concurrently with a request for continued examination under 37 C.F.R. § 1.114. Therefore, in accordance with 37 C.F.R. §1.97(b)(4), no fee or certification is due or required.

If a telephone conference would be of assistance in advancing the prosecution of the subject application, Applicant's undersigned attorney invites the Examiner to telephone him at the number provided below. However, if any fees are required in connection with the filing of this Information Disclosure Statement, authorization is hereby given to charge the amount of any such fees to Deposit Account No. 03-3125.

Respectfully submitted,



Paul Teng, Reg. No. 40,837
Attorney for Applicant
Cooper & Dunham LLP
1185 Avenue of the Americas
New York, New York 10036
(212) 278-0400



Form PTO-1449		U.S. Department of Commerce Patent and Trademark Office						Atty. Docket No. 2271/70977	Serial No. 10/656,434			
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)								Applicant: Hitoshi YAMAMOTO				
								Filing Date: September 5, 2003	Group Art Unit: 2826			
U.S. PATENT DOCUMENTS												
Examiner Initial		Document Number			Date		Name	Class	Subclass	Filing Date if Appropriate		
		5	2	4	5	2	1	5	9-14-1993	Sawaya		
FOREIGN PATENT DOCUMENTS												
		Document Number				Date	Country	Class	Subclass	Translation		
WO	0	1	/5	4	0	4	7	A1	7-26-2001	PCT	Yes	No
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)												
EXAMINER				DATE CONSIDERED								
<p>*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609: Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>												